

**DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING**

Name of the Subject : Analog and Digital Electronics Lab

Subject Code : CS306ES

Year/Sem : III-I

Regulation : R16

<b>CO1</b>	Design and test rectifiers with filters
<b>CO2</b>	Design, construct and test amplifier circuits and interpret the results.
<b>CO3</b>	Utilize the postulates of the Boolean Algebra to minimize the Combinational circuits.
<b>CO4</b>	Design and Analyze Combinational and Sequential circuits and verify the functionality.
<b>CO5</b>	Realize the logic gates using different Logic families and verify the functionality.

**Mapping Matrix of CO's and PO's:**

	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>PO9</b>	<b>P10</b>	<b>P11</b>	<b>P12</b>	<b>PSO1</b>	<b>PSO2</b>
<b>CO1</b>	X	X	X	X	X									
<b>CO2</b>	X	X	X	X	X									
<b>CO3</b>	X	X	X	X	X									
<b>CO4</b>	X	X	X	X	X									
<b>CO5</b>	X	X	X	X	X									

Course  
Coordinator

Program  
Coordinator

HOD